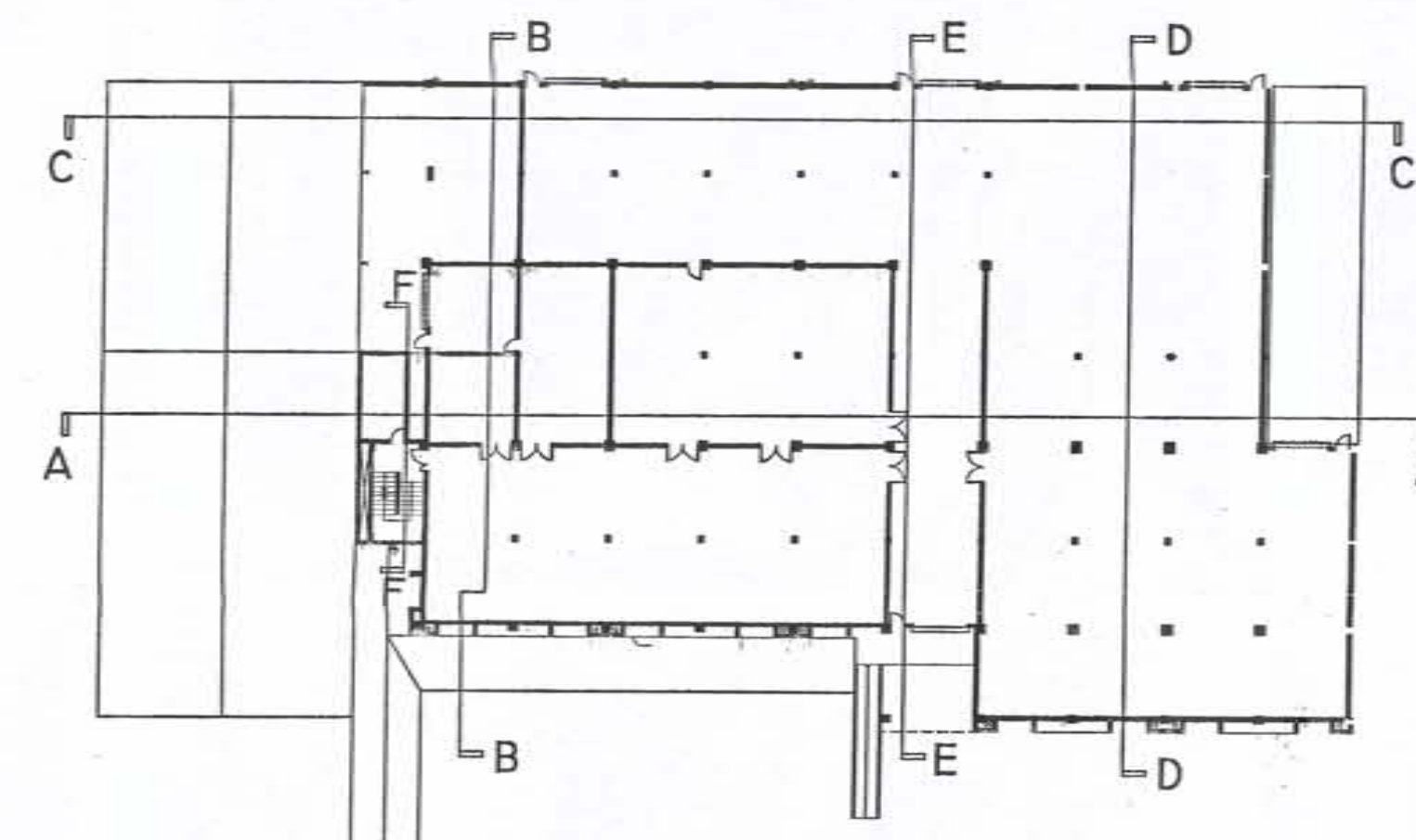
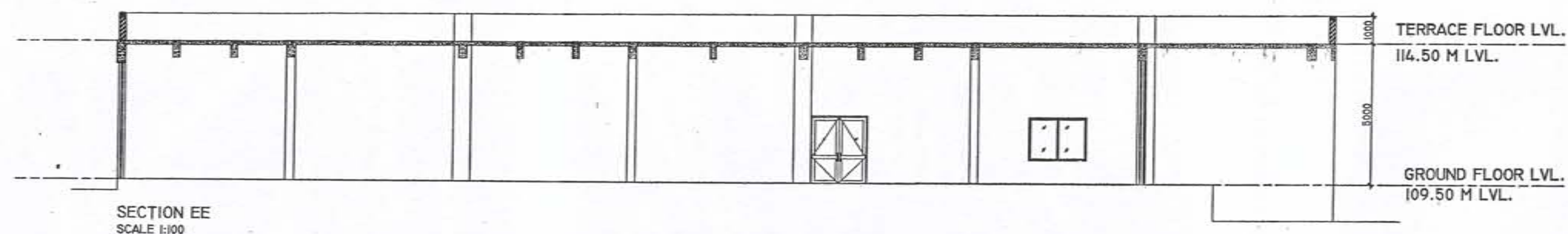
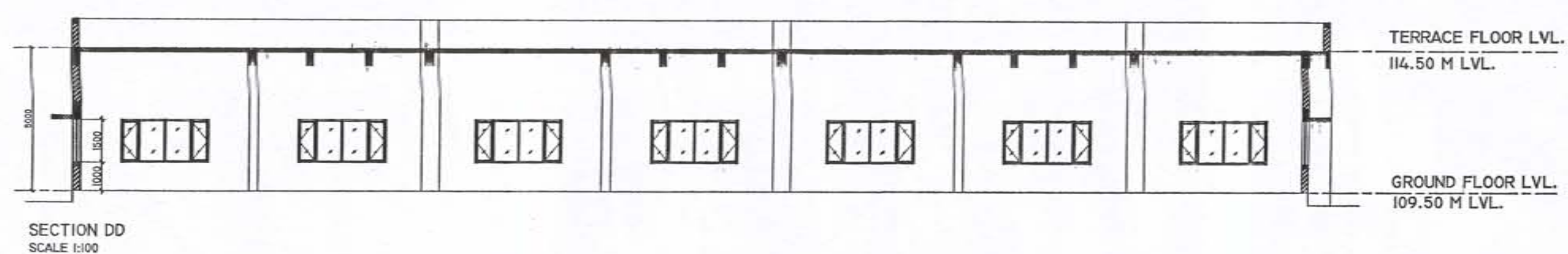
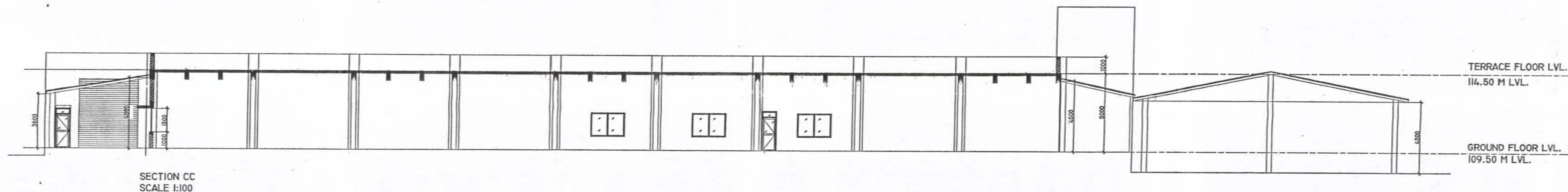
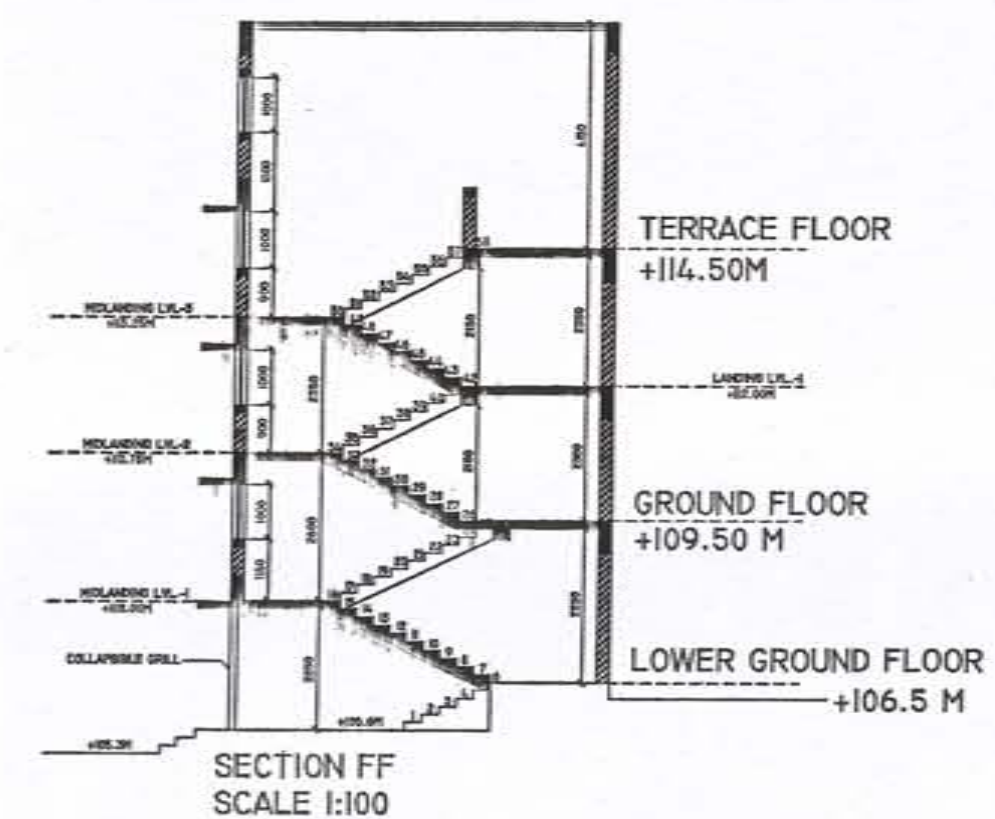
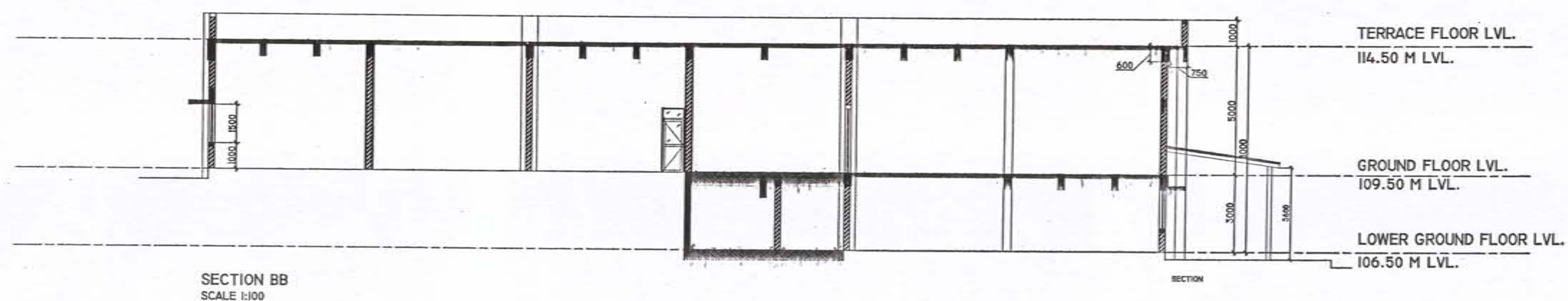
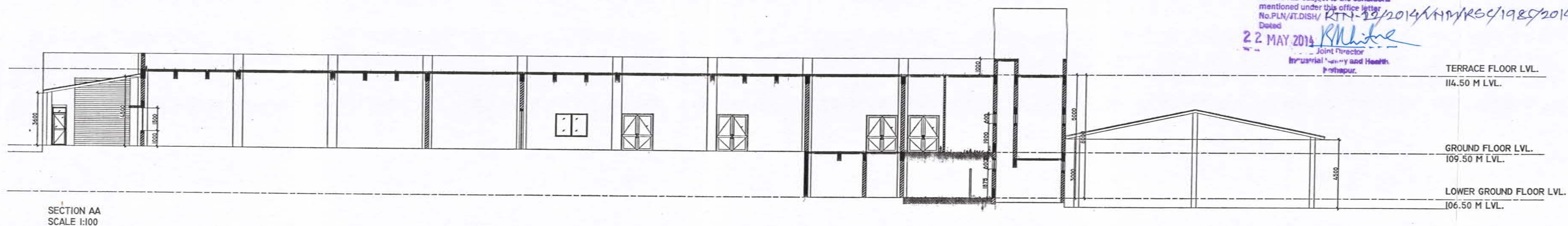


APPROVED subject to the conditions mentioned under this office letter
 No. PLN/IT.DISH/ RTN-19/2014/ITM/RSG/19&5/2014
 Dated 22 MAY 2014
 Joint Director
 Industrial, Labour and Health
 Maharashtra



PERFORMA - B

CONTENTS OF SHEET

* SECTIONS

STAMP OF DATE OF RECEPT. OF PLAN

STAMP OF DATE OF APPROVAL OF PLAN

DESCRIPTION OF PROPOSAL

PROPOSED SCHEME FOR FACTORY
 BUILDING OF AKASAKA ELECTRONICS ON
 PLOT BEARING NUMBER G-1, MIDC,
 LOTE PARSHURAM INDUSTRIAL ESTATE,
 TALUKA - KHED,
 DISTRICT - RATNAGIRI

JOB NO. DRG. NO. SCALE DATE DRN

	F-7	1:100	30-11-13	
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NAME AND SIGN. OF OWNER

M/S AKASAKA ELECTRONICS LTD.

For Akasaka Electronics Ltd.

Authorised Signatory

NAME OF THE ARCHITECT

HADRIAN LOBO

ARCHITECTS, INTERIOR DESIGNERS
 A-604, MERCURY, SUNCITY COMPLEX,
 ADI SHANKARACHARYA MARG, POWAI,
 MUMBAI 400 076

HADRIAN LOBO

A-604, MERCURY
 SUNCITY COMPLEX
 POWAI, MUMBAI 400076

NAME OF THE STRUCTURAL CONSULTANT

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