MAHARASHTRA INDUSTRIAL DEVELOPEMENT CORPORATION (A Government of Maharashtra Undertaking.)

BUILDING COMPLETION CERTIFIACATE

This is to certify that M/s.AKASAKA ELECTRONICS LTD Allotted of plot No.

G-1 in Lote -Parshuram Indl.Area Industrial Area Have completed the Factory building / Shed work on the above said plot in accordance with the building plans approved vide this office letter No.

A86054 dated 19/03/2014 through the licence Architect Architect M/s.Hadrian Lobe, Architects, design Pawai, Chartered Engineer (India), No. CA/2006/39169, Pawai, Mumbai

Detailed of units constructed are as given below: -

1) Name of allottee	:- M/s. AKASAKA ELECTRONICS LTD
2) Plot No.	:- <u>G-1</u>
3) Plot Area in Sq. m.	:- 20004.00 M2.
4) Date of allotment	:- 02/09/2009
5) Approval of plans	:-Built up Area in M2
In approval No.DE/DB/320/OF 2011.	:- 13052.45 M2
Dated:- 11/02/2011.	
II nd approval No. CPN/AE/A86054 OF 2014	:- 4071.05 M2
Dated :- 19/03/2014.	
III rd approval No	NAME OF THE PARTY
Dated :	
IV th approval No	
Dated :	
Total: One	:-4071.05 M2
afacas m, a e ga e	
6) Positions of Construction	
on the site as on 23 /07 /2014.	
v. Built up area complete in (Plinth Are	ea) :- 3455.35 M2
all respect (in Sq.m.)	
w. Aren under construction (in Sq. m.)	:-NIL
x. Open area (in Sq.m.)	:- 16548.65 M2

Remarks

regarding observation of D.C.
rules (Deputy Engineer may
indicated as to whether the
construction has been in
accordance with the approved
plan and give any other observation
which he considers necessary to give)

The Building is Constructed in accordance with building plans approved vide this office letter No. A86054 of 2014 dt. 19/03/2014.

 Area that could be considered as built up area (in Sq. m.)

4071.05 M2

13) Actual utilization of plot in view of the existing construction in the form of utilization of F.S.I.

0.226

10) Do you recommended grant of final lease of the entire plot having regard to the area of the plot and construction carried out so far?

Yes

No /DE/CPN/AE/ Lote-Par. /G-1/C22529/2014 Office of the Deputy Engineer, MIDC Chiplun Sub. Dn. CHIPLUN. Date: - 30/07/2014. Special Diffining Authority
Deputy Engineer,
MIDC, Utility Sub. Dn.
CHIPLUN.

1) Copy f.w.c.s to M/s. AKASAKA ELECTRONICS LTD. Plot No. G-1 MIDC, Lote - Parshruam Industrial Area.

2) Architect Hadrian Lobo, Architects, design Pawai, Chartered Engineer (India). No. Pawai, Mumbai.

- 3) Copy submitted to the Jt. C.E.O. Mumbai-93 for information.
- 4) Copy submitted to the Executive Engineer, MIDC, Ratnagiri for information please.
- Copy f.w.c.s to Regional officer, MIDC, Mirjole Industrial Area, Near J.K. Files Co. Ratnagiri.-415639
- 6) Copy to Kherdi-Chiplun Water Supply File.
- 7) Copy to guard file.